# Lecture 2 Learning outcomes

Upon successful completion of this module, you will be able to:

* Draw logic gates;
* Write logic equations from truth table
* Draw logic circuits from logic equation and/or truth table;
* Explain logic circuit input and output relations
* Describe the functionality of multiplexors and decoders;

# Lecture 2 Activities

**Session I:**

        ViewrecordedLecture (or slides) **Lecture2: Computer Logic;**

Review and practice the example problems presented in the lectures.

**Session II:**

Study zyBook text Appendix B 8.1-8.3;

Work on Homework H1.

**Session III:**

          Work with team members on P2 as well as on topics related to Lectures 1 & 2.

**Assignment Checklist:** -- alldue Sun 6/6

P2: Lecture 1 discussions (team)

Z2: zyBook 8.1, 8.2, 8.3

H1: Homework #1